Notice of References Cited

Application/Control No. 10/053,707	Applicant(s)/Patent Under Reexamination KAMIGATA ET AL.		
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			0
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FOREIGN PATENT DOCUMENTS

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	Q					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	"Computer Architecture A Quantitative Approach", Second Edition, David A. Patterson and John L. Hennessy, 1996, Chapter 4, pages 220 - 362			
	٧	Intel Technology Journal 4 th Quarter 1999, An Overview of the Intell IA-64 Compiler, Carole Dulong et al. pages 1 - 15			
	w	HP Laboratories Technical Report Machine Description Driven Compiler for EPIC Processors, B. Ramakrishna Rau et al, September 1998, pages 1 82			
	x				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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